

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
UTILITY PATENT APPLICATION TRANSMITTAL LETTER

Attorney Docket No.: ONS00507
Mailing Date: March 8, 2004

To: Assistant Commissioner for Patents
Mail Stop Patent Application
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Re Attorney Docket No. **ONS00507**, transmitted herewith is an application for Nonprovisional Utility Patent, entitled

HIGH VOLTAGE LATERAL FET STRUCTURE WITH IMPROVED ON RESISTANCE PERFORMANCE

for filing under 37 C.F.R. §1.53(b) as a New Patent Application.

The inventor(s) is/are **Rajesh S. Nair, Shanghui Larry Tu, Zia Hossain, and Mohammed Tanvir Quddus**

Patent application filing papers include,

- ☒ 17 page(s) of specification and claims, and three (3) sheets of informal drawings.
- ☒ Newly executed oath or declaration combined with power of attorney on 3 page(s).
- ☒ An assignment transmittal letter and assignment of the invention to Semiconductor Components Industries, LLC.
- ☒ Information Disclosure Statement PTO-1449.
- ☒ Return Receipt Postcard.
- ☒ The filing fee is calculated as follows:

CLAIMS AS FILED, LESS ANY CANCELED BY AMENDMENT

CLAIMS	In Appln.	Allowed	Excess	Fee	Total Fee
Total	20	20	0	\$18.00	\$0.00
Independent	3	3	0	\$86.00	\$0.00
Basic Filing Fee					\$770.00
Total					\$770.00

- ☒ Please charge Deposit Account No. 501086 in the Total amount shown above, and any Extension Fee under 37 C.F.R. §1.136(a) if applicable.
- ☒ The Commissioner is hereby authorized to charge any additional fees which may be required now or in the future during the entire pendency of this application under 37 C.F.R. 1.16 or 37 C.F.R. 1.17, including any present or future time extension fees which may be required, or credit any overpayment to Deposit Account No. 501086.
- ☒ This sheet is submitted in duplicate.

This transmittal letter has 1 total pages.

DATE

3/8/04
ON SEMICONDUCTOR
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